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**Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231**

New U.S. Patent Application

Title: METHOD FOR ESTIMATING POWER CONSUMPTION AND NOISE
LEVELS OF AN INTEGRATED CIRCUIT, AND COMPUTER-READABLE RECORDING
MEDIUM STORING A PROGRAM FOR ESTIMATING POWER CONSUMPTION AND
NOISE LEVELS OF AN INTEGRATED CIRCUIT

Inventor and Addressee:

(1) Taku UCHINO
Tokyo, JAPAN

Sir:

We enclose the following papers for filing in the United States Patent and
Trademark Office in connection with the above patent application.

1. A check for \$690 representing the filing fee.
2. Application - 31 pages, including 2 independent claims and 12 claims total.
3. Drawings - 6 sheets of formal drawings containing 15 figures.

This application is being filed under the provisions of 37 C.F.R. § 1.53(f).
Applicant awaits notification from the Patent and Trademark Office of the time set for
filing the Declaration.

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Applicant claims the right to priority based on Japanese Patent Application No. 11-264538, filed September 17, 1999.

Please accord this application a serial number and filing date.

The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or § 1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
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By:



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RVB/FPD/mld
Enclosures